

GLITCH REMOVAL CIRCUIT

ABSTRACT OF THE INVENTION

A glitch removal circuit that removes both positive and negative glitches from an input signal includes a delay circuit, a glitch blocking circuit, and a latch circuit. The delay circuit receives the input signal and introduces a delay into it. The glitch blocking circuit is coupled to the delay circuit, and includes two NMOS transistors and two PMOS transistors. The glitch blocking circuit receives the input signal and the delayed input signal and blocks the input signal if there is a glitch in it. The latch circuit is coupled to the output of the glitch blocking circuit. The latch circuit inverts the output of the glitch blocking circuit and stores the output on a continuous basis. The latch circuit provides glitch free signal as the output.